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				Application Number	10/566,628
				Filing Date	February 1, 2006
				First Named Inventor	ROTH, Shmuel
				Art Unit	2873
(use as many sheets as necessary)				Examiner Name	PATEL, VIPIN
Sheet	1	of	1	Attorney Docket Number	P-6042-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A	SHIMIZU, "Invited Paper: Scrolling Color LCOS for HDTV Rear Projection", SID 01 Digest, pp. 1072-1075; 2001	<input type="checkbox"/>
	B	IMAI, "Spectral Reproduction from Scene to Hardcopy, Part 1-Multi-spectral Acquisition and Spectral Estimation using a Trichromatic Digital Camera System Associated with Absorption Filters", Munsell Color Science Laboratory, Rochester Institute of Technology; 2000	<input type="checkbox"/>
	C	AJITO et al., "Expanded Color Gamut Reproduced by Six-primary Projection Display", Proc. SPIE, Vol. 2954 (2000) pp.130-37	<input type="checkbox"/>
	D	HORIBE et al., "High Efficiency and High Visual Quality LCD Backlighting System", Faculty of Science and Technology, Keio University, Japan, pp. 1-4; 1998	<input type="checkbox"/>
	E	YAMAGUCHI et al., "Color Image Reproduction Based on the Multispectral and Multiprimary Imaging: Experimental Evaluation", Device Independent Color, Color Hardcopy and Applications VII, Proc SPIE, Vol. 4663, pp.15-26; 2002	<input type="checkbox"/>
	F	PLATT, "Optimal Filtering for Patterned Displays", Microsoft Research, pp. 1-4; Signal Processing Letters IEEE; Vol. 7; Issue 7; 2000	<input type="checkbox"/>
	G	TAKATORI et al., "Field-Sequential Smectic LCD with TFT Pixel Amplifier", Functional Devices Research Labs, NEC Corp., Kawasaki, Kanagawa 216-8555, Japan, SID 01 Digest, pp. 48-51; 2001	<input type="checkbox"/>
	H	ELLIOTT et al., "Co-Optimization of Color AMLCD Subpixel Architecture and Rendering Algorithms", ClairVoyante Laboratories, USA and AMLCD, Semiconductor Business, Korea, pp. 1-4; 2002	<input type="checkbox"/>
	I	ELLIOTT, "Active Matrix Display Layout Optimization for Sub-pixel Image Rendering", ClairVoyante Laboratories, USA, pp. 1-5; 2000	<input type="checkbox"/>
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Examiner Signature	Date Considered
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